



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Farnworth et al.

**Serial No.:** 09/589,841

**Filed:** June 8, 2000

**For:** STEREOLITHOGRAPHIC  
METHODS FOR FORMING A  
PROTECTIVE LAYER ON A  
SEMICONDUCTOR DEVICE SUBSTRATE  
AND SUBSTRATES INCLUDING  
PROTECTIVE LAYERS SO FORMED

**Confirmation No.:** 1415

**Examiner:** G. Lee

**Group Art Unit:** 2825

**Attorney Docket No.:** 2269-3923US  
(99-0033.00/US)

**Notice of Allowance Mailed:**

August 27, 2004

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**AMENDMENT PURSUANT TO 37 C.F.R. § 1.312(a)**

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Please amend the above-referenced application as follows:

**Serial No. 09/589,841**

**Amendments to the Specification** begin on page 3 of this paper.

**A Listing of the Claims** begins on page 8 of this paper.

**Remarks** begin on page 17 of this paper.